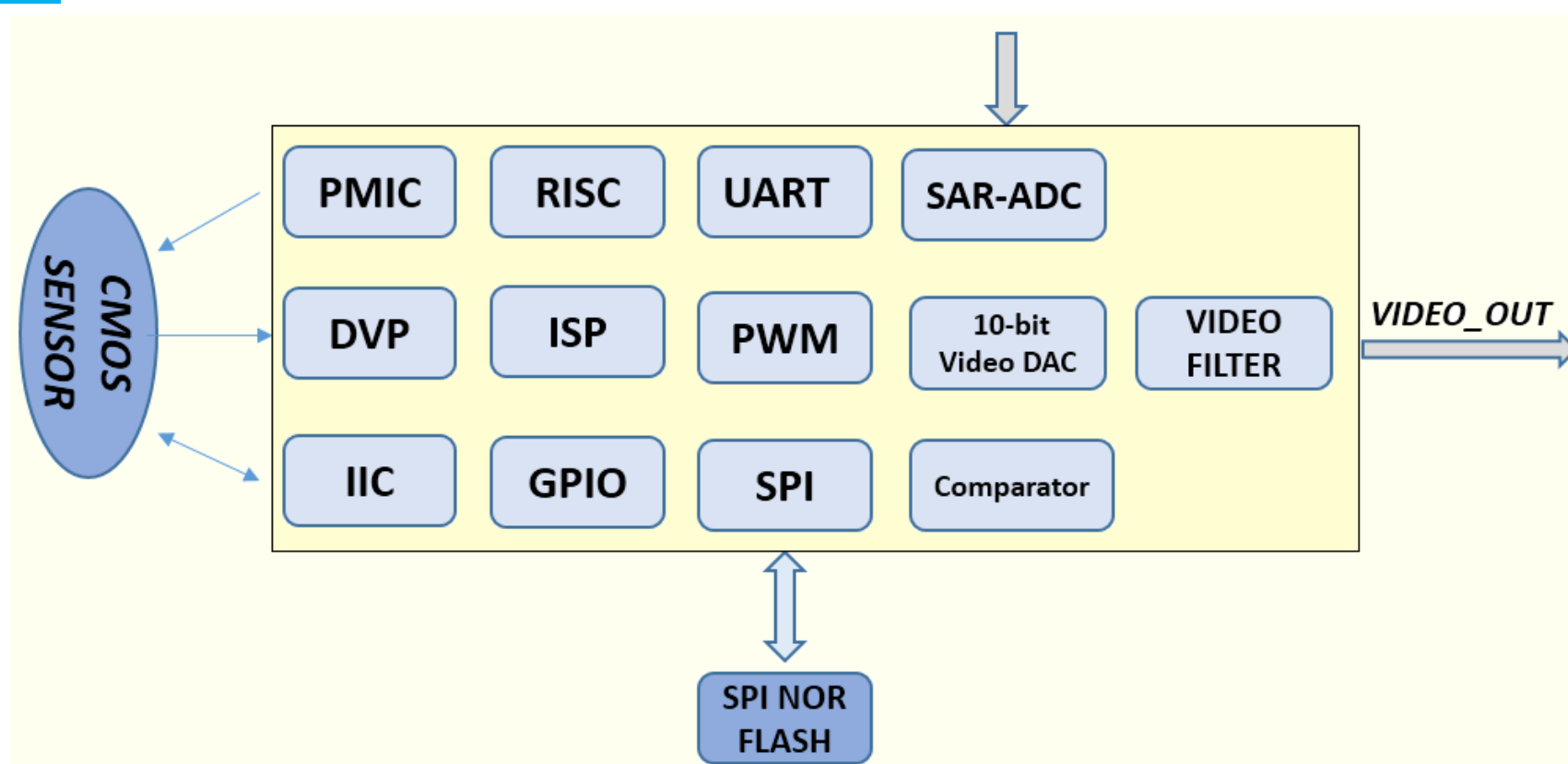# FH8537 : High Performance and Cost Effective, High Integration 2M ISP for CCTV

FH8537 is a new generation image signal processing chip for Parallel CIS (CMOS Image Sensor).It supports 2.0/1.3/1.0Mega image input, supports standard CVBS, 960H, 1280H and analog HD output, and built-in PMIC power module.It can be used in highly competitive industry analog HD cameras.
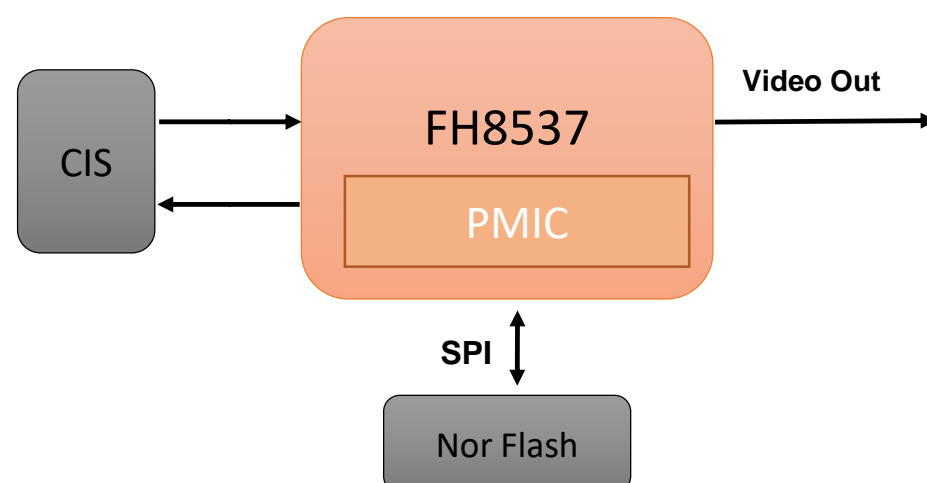
Features:

CIS Input	Support 2M/1.3M/1.0M CMOS sensor RGB / RGBIR 4X4 , 10bit data bit with	Video Output	Standard CVBS（1280H）
			HD Analog ： 1080P, 720P
	Clock positive and negative edge sampling is configurable	Interface	I2C
ISP Function	High performance CFA interpolation		SPI
	Adaptive 2D and Y/C denoising		UART
	AE/AWB		GPIO
	Lens Shade Correction		SADC
	Image enhancement		PWM
	Automatic detection and elimination of dead pixels	Others	Work Temperature：-20℃ ~ 70℃
			Power Supply: Analog 3.3V ; IO(3.3V or 1.8V) ; Digital (1.1V)
OSD Function	Customized TOSD/GOSD	Package	QFN56 (7*7mm)

Diagram：

Typical Application Solution: